

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/760,405	NIE, XIAONING	
Examiner		Art Unit		Page 1 of 2
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,530,816	06-1996	Holt, Nicholas P.	712/217
*	B	US-5,546,593	08-1996	Kimura et al.	712/228
*	C	US-5,574,939	11-1996	Keckler et al.	712/24
*	D	US-5,742,782	04-1998	Ito et al.	712/210
*	E	US-5,812,811	09-1998	Dubey et al.	712/216
*	F	US-5,944,816	08-1999	Dutton et al.	712/215
*	G	US-6,105,127	08-2000	Kimura et al.	712/215
*	H	US-6,170,051	01-2001	Dowling, Eric M.	712/225
*	I	US-6,363,475	03-2002	Dowling, Eric M.	712/206
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tullsen, Dean M.; Eggers, Susan J.; and Levy, Henry M. "Simultaneous Multithreading: Maximizing On-Chip Parallelism". ©1995 ACM. Pages 533-544.
	V	Tullsen, Dean M.; Eggers, Susan J.; Emer, Joel S.; Levy, Henry M.; Lo, Jack L.; and Stamm, Rebecca L. "Exploiting choice: instruction fetch and issue on an implementable simultaneous multiprocessor". ©1996 ACM SIGARCH Computer Architecture News. Pages 191-202.
	W	Tullsen, Dean M.; Eggers, Susan J.; Emer, Joel S.; Levy, Henry M.; Lo, Jack L.; and Stamm, Rebecca L. "Simultaneous Multithreading: A Platform for Next Generation Processors" ©1997 IEEE Micro. Pages 12-19.
	X	Tullsen, Dean M.; Eggers, Susan J.; Emer, Joel S.; Levy, Henry M.; Lo, Jack L.; and Stamm, Rebecca L. "Converting Thread-Level Parallelism to Instruction-Level Parallelism via Simultaneous Multithreading" ©1997 ACM Transactions on Computer Systems. Pages 322-354.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	A	US-			
	B	US-			
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	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hily, Sebastien and Seznec, Andre. "Standard Memory Hierarchy Does Not Fit Simultaneous Multithreading". ©1998. Pages 1-8.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.